



RESPONSE UNDER 37 C.F.R. §1.116
-EXPEDITED PROCEDURE-
EXAMINING GROUP 2836

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S. Pontarollo
Serial No: 08/653,958
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Examiner: Leja, R.
Art Unit: 2836

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

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Sir:

AMENDMENT AFTER FINAL UNDER RULE 1.116

In response to the Final Office Action mailed December 8, 1998, Applicant respectfully requests reconsideration. To further the prosecution of this application, Applicant has amended claims, and submits the following remarks.

IN THE CLAIMS

1. (Thrice Amended) A device for protecting a circuit against voltage surges, comprising:

[a] an MOS transistor of a first type directly connected to first and second supply terminals by its source and its drain, respectively;

[a] an MOS transistor of a second type directly connected between the second supply terminal and the gate of the transistor of the first type by its source and its drain, respectively;
and